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Appl. No. 10/607,869

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/607,869
Filing Date	June 27, 2003
Inventor	
Assignee	
Group Art Unit	2812
Examiner	
Attorney's Docket No.	
Customer No.	
Title: Method of Forming Silicon-on-Insulator Comprising Integrated Circuitry	

## **RESPONSE TO JUNE 25, 2004 OFFICE ACTION**

To:

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

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## **AMENDMENTS**

## **Introductory Comments**

In reply to the Office Action of June 25, 2004, applicant amends and remarks as follows.